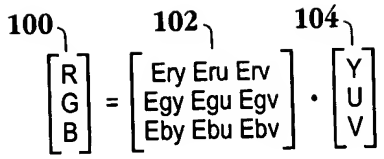

Fig. 1 *(prior art)*

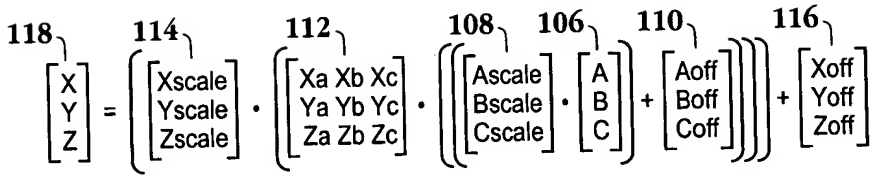

Fig. 2

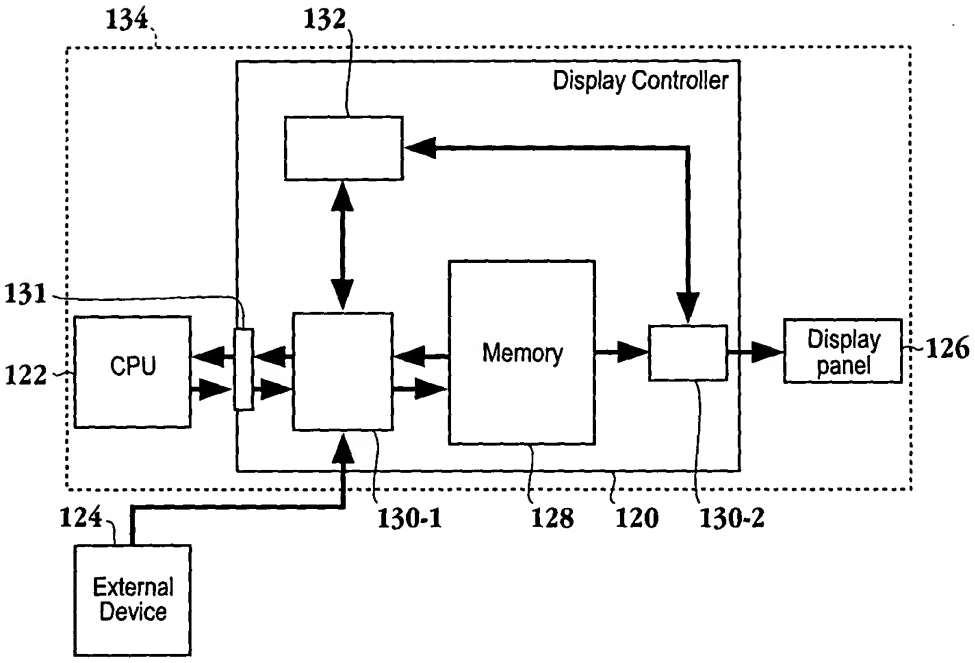
134
132
Display Controller
131
122
CPU
Memory
Display panel
126
124
External Device
130-1
128
120
130-2

Fig. 3

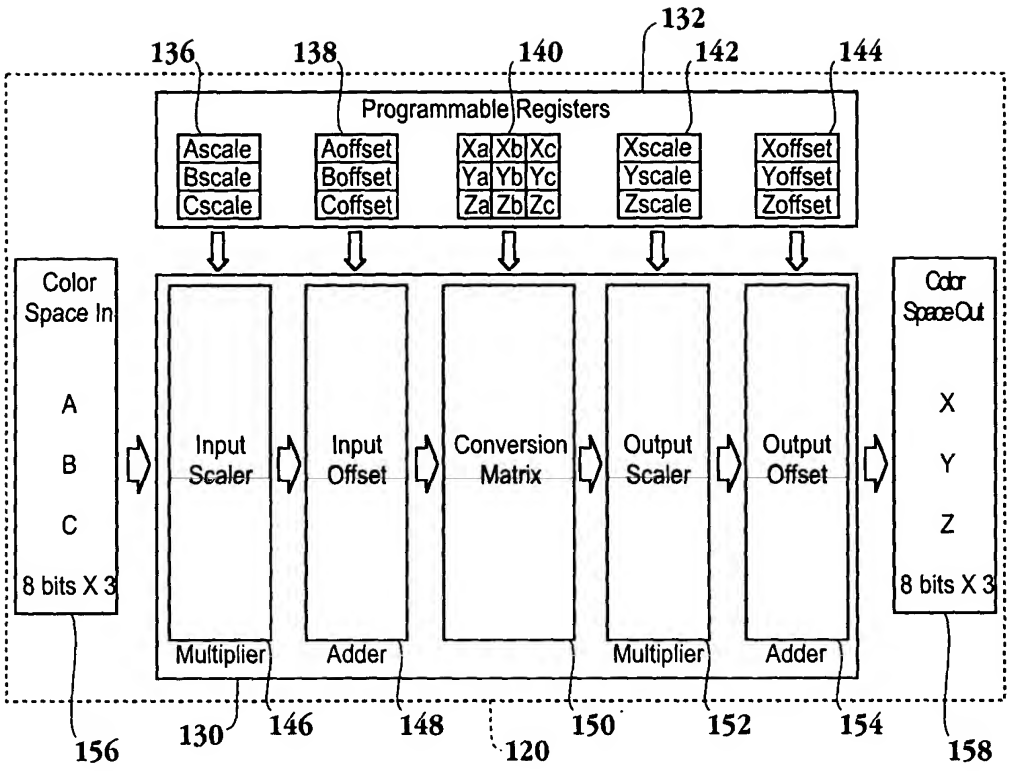
132
136
138
140
142
144
Programmable Registers
Ascale
Bscale
Cscale
Aoffset
Boffset
Coffset
Xa Xb Xc
Ya Yb Yc
Za Zb Zc
Xscale
Yscale
Zscale
Xoffset
Yoffset
Zoffset
Color Space In
A
B
C
8 bits X 3
Input Scaler
Input Offset
Conversion Matrix
Output Scaler
Output Offset
Multiplier
Adder
Multiplier
Adder
Color Space Out
X
Y
Z
8 bits X 3
156
130
146
148
120
150
152
154
158

Fig. 4

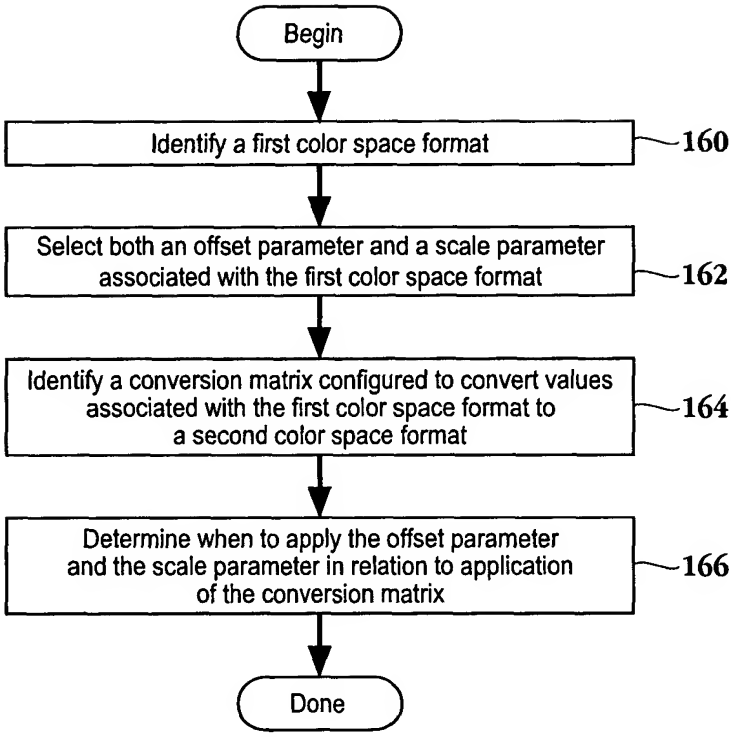
Begin
Identify a first color space format
160
Select both an offset parameter and a scale parameter associated with the first color space format
162
Identify a conversion matrix configured to convert values associated with the first color space format to a second color space format
164
Determine when to apply the offset parameter and the scale parameter in relation to application of the conversion matrix
166
Done

Fig. 5